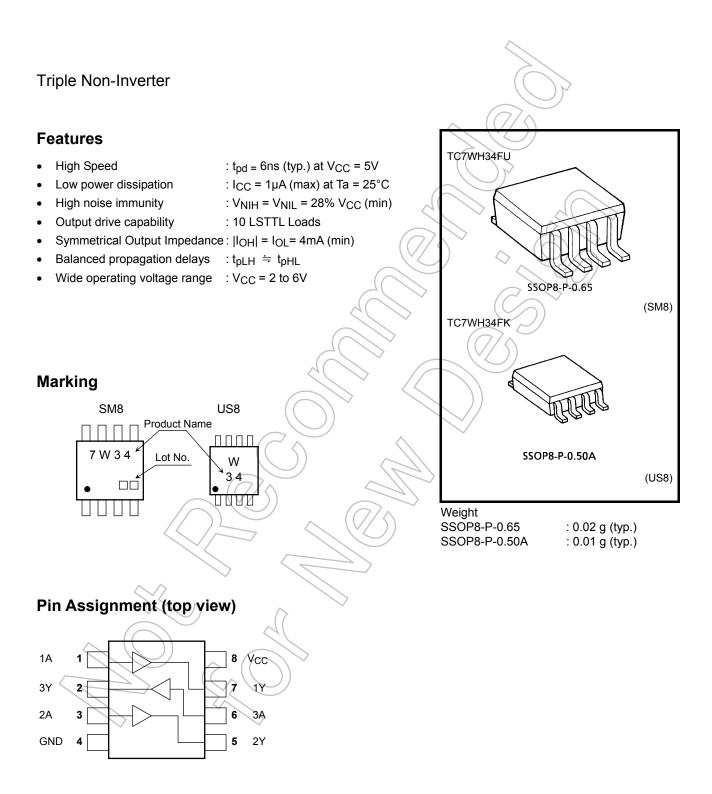
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7W34FU, TC7W34FK



#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	–0.5 to 7.0	V	
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5		
DC output voltage	Vout	–0.5 to V <sub>CC</sub> + 0.5	V	
Input diode current	I <sub>IK</sub>	±20	mA	
Output diode current	Іок	±20	mA	
DC output current	lout	±25	mA	
DC V <sub>CC</sub> /ground current	Icc	±50	mA	
Dower discinction	D-	300 (SM8)		
Power dissipation	PD	200 (US8)	mW	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# IEC Logic Symbol

#### **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 6.0	V
Input voltage	VIN	0 to V <sub>CC</sub>	V
Output voltage	VOUT	0 to V <sub>CC</sub>	V
Operating temperature	Topr	-40 to 85	°C
		0 to 1000 ( $V_{CC} = 2.0 \text{ V}$ )	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 $(V_{CC} = 4.5 V)$	ns
		0 to 400 $(V_{CC} = 6.0 \text{ V})$	

#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol Test		Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
		V <sub>CC</sub>		Min	Тур.	Max	Min	Max	Unit	
				2.0	1.5	_	$\mathcal{A}$	1.5	_	
High-level input voltage VIH –		—		4.5	3.15	_	Â	3.15		
			6.0	4.2	_	A	4.2	_	V	
						-((	0.5		0.5	v
Low-level input voltage V <sub>IL</sub>	V <sub>IL</sub>	—		4.5		$\geq$	1.35	—	1.35	
				6.0	_	$\langle \frown \rangle$	1.8	—	1.8	
High-level output voltage	V <sub>OH</sub> V <sub>II</sub>			2.0	1.9	2.0	$)^{\prime}-$	1.9		
			I <sub>OH</sub> = –20 μA	4.5	4.4	4.5	—	4.4	/	
		$V_{IN} = V_{IH}$		6.0	5.9	6.0	—	5.9	K	
			I <sub>OH</sub> = -4 mA	4.5	4.18	<sup>∨</sup> 4.31	- (	4.13	$\geq -$	
			I <sub>OH</sub> = -5.2 mA	6.0	5.68	5.80 <		5.63	) -	v
Low-level output voltage V		V <sub>OL</sub> V <sub>IN</sub> = V <sub>IL</sub>		2.0		0.0	0.1	F	0.1	v
			I <sub>OL</sub> = 20 μA	4.5	>	0.0	0.1	<u> </u>	0.1	
	V <sub>OL</sub>			6.0		0.0	0.1	—	0.1	
			I <sub>OL</sub> = 4 mA	4.5		0.17	0.26	_	0.33	
			I <sub>OL</sub> = 5.2 mA	6.0		0.18	0.26	_	0.33	
Input leakage current	I <sub>IN</sub>	$V_{IN} = V_{CC}$	or GND	6.0 <	<-	$\mathcal{H}$	±.0.1	—	±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$	or GND	6.0	X	H	1.0	—	10.0	μA

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## AC Characteristics ( $C_L$ = 15pF, $V_{CC}$ = 5V, Ta = 25°C)

Characteristics	Symbol	Test Condition	Ta = 25°C			Unit
	Symbol		Min	Тур.	Max	Onit
Output Transition Time	t <sub>TLH</sub>	—		4	8	ns
	t <sub>THL</sub>			0	115	
Propagation Delay Time	t <sub>pLH</sub>	_	6 12	12	ns	
	t <sub>pLH</sub>		( )		12	115

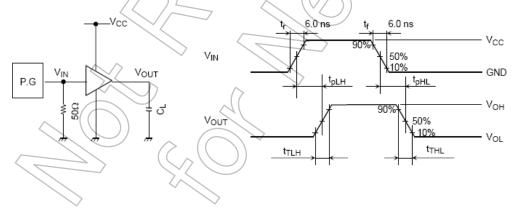
#### AC Characteristics ( $C_L$ = 50pF, Input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics Symbo	Symbol	Test Condition		Ta = 25°C		$Ta = -40$ to $85^{\circ}C$		Unit	
	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Onit
Output Transition Time	<b>4</b>	tTLH	2.0	4	30	75	A	95	
			4.5	()	8	15	$\leq \geq$	19	ns
	THL	6.0	$\langle - \rangle$	7 <	13		16		
Propagation delay time $t_{pH}$	t <sub>pLH</sub>	_	2.0		27	75	TH)	95	
			4.5	>	9	15	>	19	ns
	чрн∟		6.0	_	8	13)	—	16	
Input capacitance	C <sub>IN</sub>	-(	$\sim$		5	10		10	pF
Power dissipation capacitance	C <sub>PD</sub>		(Note 1)		20	<i>V</i> _		_	pF

Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:

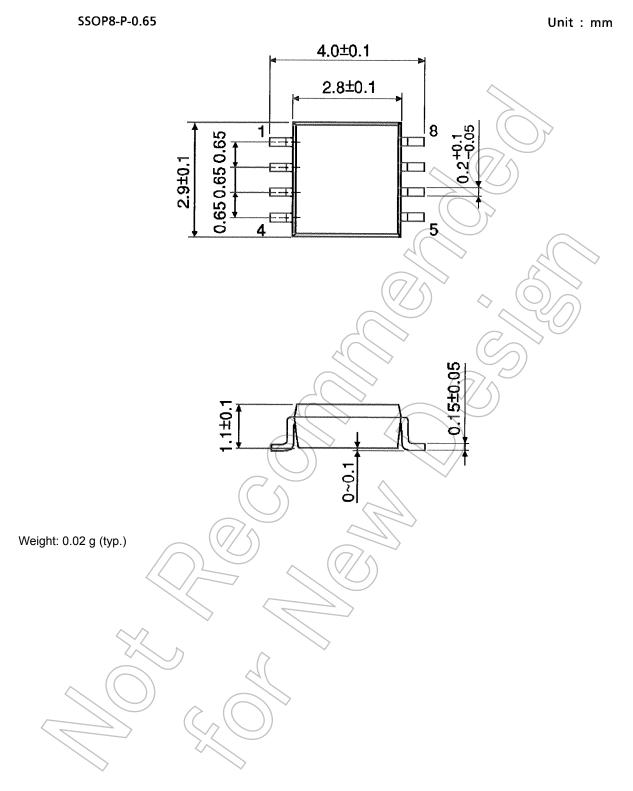
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ 

#### Switching characteristics test circuit



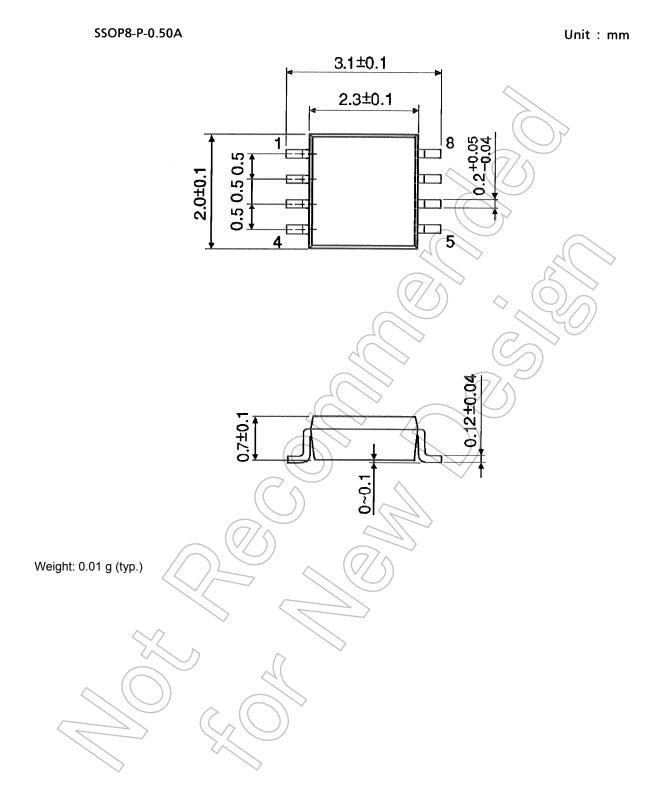
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### Package Dimensions



## <u>TOSHIBA</u>

#### Package Dimensions



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