

Overview

KEMET's Automotive Grade Series surface mount capacitors in COG dielectric are suited for a variety of applications requiring proven, reliable performance in harsh environments. Whether under-hood or in-cabin, these devices emphasize the vital and robust nature of capacitors required for mission and safety critical automotive circuits. Stricter testing protocol and inspection criteria have been established for automotive grade products in recognition of potentially harsh environmental conditions. KEMET automotive grade series capacitors meet the demanding Automotive Electronics Council's AEC–Q200 qualif cation requirements. COG dielectric features a 125°C maximum operating temperature and is considered "stable." The Electronics Industries Alliance (EIA) characterizes COG dielectric as a Class I material. Components of this classif cation are temperature compensating and are suited for resonant circuit applications or those where Q and stability of capacitance characteristics are required. COG exhibits no change in capacitance with respect to time and voltage and boasts a negligible change in capacitance with reference to ambient temperature. Capacitance change is limited to ±30 ppm/°C from -55°C to +125°C.



250 V

Capacitancefferings ranging from 0.5 pF up to 0.47 F Available capacitance tolerances of ±0.10 pF, ±0.25 pF, ±0.5 pF, ±1 ±2 ±5 ±10 and ±20

Ordering Information

С	1206	C	104	J	3	G	Α	C	AUTO
Ceramic	Case Size (L" x W")	Specifcation/ Series	Capacitance Code (pF)	Capacitance Tolerance ¹	Rated Voltage (VDC)	Dielectric	Failure Rate/ Design	Termination Finish ²	Packaging/Grade (C-Spec)
	0402 0603 0805 1206 1210 1812 2220	C = Standard	Two signif cant digits + number of zeros Use 9 for 1.0 – 9.9 pF Use 8 for 0.5 – .99 pF ex. 2.2 pF = 229 ex. 0.5 pF = 508	$B = \pm 0.10 \text{ pF} \\ C = \pm 0.25 \text{ pF} \\ D = \pm 0.5 \text{ pF} \\ F = \pm 1\% \\ G = \pm 2\% \\ J = \pm 5\% \\ K = \pm 10\% \\ M = \pm 20\%$	8 = 10 4 = 16 3 = 25 5 = 50 1 = 100 2 = 200 A = 250	G = COG	A = N/A	C = 100% Matte Sn	See "Packaging C-Spec Ordering Options Table" below

¹Additional capacitance tolerance offerings may be available. Contact KEMET for details.

² Additional termination finish options may be available. Contact KEMET for details.



Packaging C-Spec Ordering Options Table

Packaging Type ¹	Packaging/Grade Ordering Code (C-Spec) ³				
7" Reel	AUTO				
13" Reel/Unmarked	AUTO7411 (EIA 0603 and smaller case sizes) AUTO7210 (EIA 0805 and larger case sizes)				
7" Reel/Unmarked/2 mm pitch ²	3190				
13" Reel/Unmarked/2 mm pitch ²	3191				

¹ Reeling tape options (Paper or Plastic) are dependent on capacitor case size (L" x W") and thickness dimension. See "Chip Thickness/Tape & Reel Packaging Quantities" and "Tape & Reel Packaging Information".

² The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

³ All Automotive packaging C-Specs listed exclude the option to laser mark components. Please contact KEMET if you require a laser marked option. For more information see "Capacitor Marking".

³ For additional Information regarding "AUTO" C-Spec options, see "Automotive C-Spec Information".

Benefits cont'd

- · No piezoelectric noise
- Extremely low ESR and ESL
- High thermal stability
- High ripple current capability
- Preferred capacitance solution at line frequencies and into 100% pure matte tin-plated termination fnish allowing for the MHz range
- No capacitance change with respect to applied rated DC voltage
- Negligible capacitance change with respect to temperature from -55°C to +125°C
- · No capacitance decay with time
- Non-polar device, minimizing installation concerns
- excellent solderability
- SnPb plated termination fnish option available upon request (5% minimum)

Applications

Typical applications include critical timing, tuning, circuits requiring low loss, circuits with pulse, high current, decoupling, bypass, fltering, transient voltage



Automotive C-Spec Information

KEMET Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC–Q200, Stress Test Qualifcation for Passive Components. These products are supported by a Product Change Notifcation (PCN) and Production Part Approval Process warrant (PPAP).

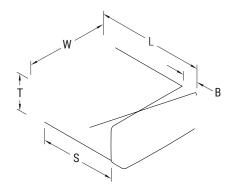
Automotive products offered through our distribution channel have been assigned an inclusive ordering code C-Spec, "AUTO". This C-Spec was developed in order to better serve small and medium sized companies that prefer an automotive grade component without the requirement to submit a customer Source Controlled Drawing (SCD) or specification for review by a KEMET engineering specialist. This C-Spec is therefore not intended for use by KEMET'S OEM Automotive customers and are not granted the same "privileges" as other automotive C-Specs. Customer PCN approval and PPAP request levels are limited (see details below).

Product Change Notification (PCN)

The KEMET Product Change Notif cation system is used to communicate primarily the following types of changes:



Dimensions – Millimeters (Inches)





Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	±30 ppm/°C
Aging Rate (Maximum % Capacitance Loss/Decade Hour)	0%
¹ Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5±1 seconds and charge/discharge not exceeding 50 mA)
² Dissipation Factor (DF) Maximum Limit at 25°C	0.1%
³ Insulation Resistance (IR) Limit at 25°C	1,000 megohm microfarads or 100 GΩ (Rated voltage applied for 120±5 seconds at 25°C)

¹ DWV is the voltage a capacitor can withstand (survive) for a short period of time. It exceeds the nominal and continuous working voltage of the capacitor.

² Capacitance and dissipation factor (DF) measured under the following conditions:

1 MHz ±100 kHz and 1.0 Vrms ±0.2 V if capacitance \leq 1,000 pF

1 kHz ±50 Hz and 1.0 Vrms ±0.2 V if capacitance > 1,000 pF

³ To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to G Ω limit. Select the lower of the two limits.

Capacitance and Dissipation Factor (DF) measured under the following conditions:

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

Post Environmental Limits

	High Temperature Life, Biased Humidity, Moisture Resistance									
Dielectric	Rated DC Voltage	Capacitance Value	Dissipation Factor (Maximum %)	Capacitance Shift	Insulation Resistance					
COG	All	All	0.5	0.3% or ±0.25 pF	10% of Initial Limit					





Table 2 – Chip Thickness/Tape & Reel Packaging Quantities

Thickness	Case	Thickness ±	Paper Q	uantity ¹	Plastic (Quantity
Code	Size1	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel
BB BD	0402 0402	0.50 ± 0.05 0.55 ± 0.05	10,000	50,000	0	0



Table 3 – Chip Capacitor Land Pattern Design Recommendations per IPC-7351

EIA Size Code	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)					Density Level B: Median (Nominal) Land Protrusion (mm)				Density Level C: Minimum (Least) Land Protrusion (mm)					
Coue	Coue	C	Y	X	V1	V2	C	Y	X	V1	V2	C	Y	X	V1	V2
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90
1210 ¹	3225	1.50	1.60	2.90	5.60	3.90	1.40	1.40	2.80	4.70	3.30	1.30	1.20	2.70	4.00	3.00
1812	4532	2.15	1.60	3.60	6.90	4.60	2.05	1.40	3.50	6.00	4.00	1.95	1.20	3.40	5.30	3.70
2220	5650	2.75	1.70	5.50	8.20	6.50	2.65	1.50	5.40	7.30	5.90	2.55	1.30	5.30	6.60	5.60

¹ Only for capacitan



Soldering Process

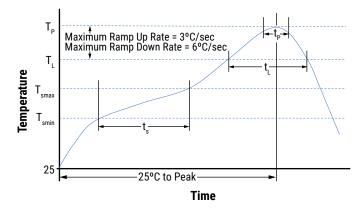
Recommended Soldering Technique:

- Solder wave or solder refow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder refow only

Recommended Reflow Soldering Profile:

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase refow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profle conditions for convection and IR refow refect the profle conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three refow passes at these conditions.

Profile Feature	Termination Finish				
Fromereature	SnPb	100% Matte Sn			
Preheat/Soak					
Temperature Minimum (T _{smin})	100°C	150°C			
Temperature Maximum (T _{Smax})	150°C	200°C			
Time (t_s) from T_{smin} to T_{smax}	60 – 120 seconds	60 – 120 seconds			
Ramp-Up Rate (T_L to T_p)	3°C/second maximum	3°C/second maximum			
Liquidous Temperature (T _L)	183°C	217°C			
Time Above Liquidous (t_L)	60 – 150 seconds	60 – 150 seconds			
Peak Temperature (T _P)	235°C	260°C			
Time Within 5°C of Maximum Peak Temperature (t _p)	20 seconds maximum	30 seconds maximum			
Ramp-Down Rate $(T_p to T_L)$	6°C/second maximum	6°C/second maximum			
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum			



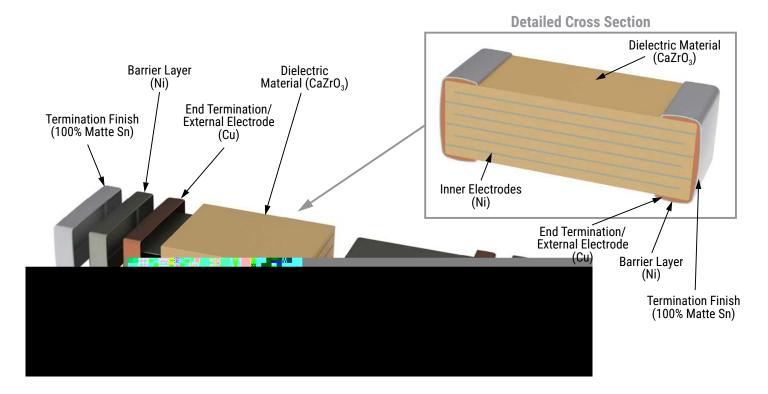
Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.

Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature–reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fuctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction



Capacitor Marking (Optional):

Laser marking option is not available on:

- COG, Ultra Stable X8R and Y5V dielectric devices
- EIA 0402 case size devices
- EIA 0603 case size devices with Flexible Termination option.
- KPS Commercial and Automotive grade stacked devices.

These capacitors are supplied unmarked only.



Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

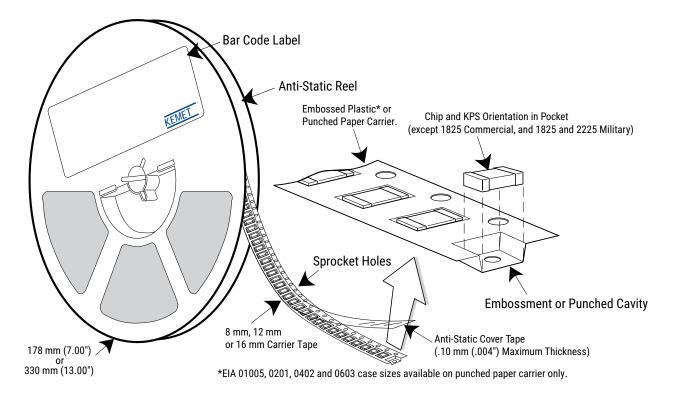


Table 5 – Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

	Таре	Embosse	d Plastic	Punched Paper		
EIA Case Size	Size	7" Reel	13" Reel	7" Reel	13" Reel	
	(W)*	Pitch	(P ₁)*	Pitch (P ₁)*		
01005 - 0402	8			2	2	
0603	8			2/4	2/4	
0805	8	4	4	4	4	
1206 - 1210	8	4	4	4	4	
1805 - 1808	12	4	4			
≥ 1812	12	8	8			
KPS 1210	12	8	8			
KPS 1812 & 2220	16	12	12			
Array 0508 & 0612	8	4	4			

*Refer to Figures 1 & 2 for W and P_{γ} carrier tape reference locations. *Refer to Tables 6 & 7 for tolerance specifications.

New 2 mm Pitch Reel Options*

Packaging Ordering Code (C-Spec)	Packaging Type/Options
C-3190	Automotive grade 7" reel unmarked
C-3191	Automotive grade 13" reel unmarked
C-7081	Commercial grade 7" reel unmarked
C-7082	Commercial grade 13" reel unmarked

* 2 mm pitch reel only available for 0603 EIA case size. 2 mm pitch reel for 0805 EIA case size under development.

Benefits of Changing from 4 mm to 2 mm Pitching Spacing

- Lower placement costs
- Double the parts on each reel results in fewer reel changes and increased effciency
- Fewer reels result in lower packaging, shipping and storage costs, reducing waste



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

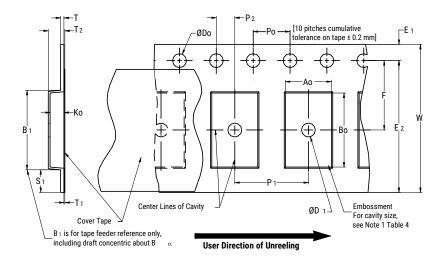


Table 6 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions – Millimeters (Inches)								
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm		1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/- 0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm	,	(0.059)				(1.181)			
	Variable Dimensions – Millimeters (Inches)								
Tape Size	Pitch	B ₁ Maximum Note 4	E ₂ Minimum	F	P ₁	T ₂ Maximum	W Maximum	A ₀ ,B ₀	. & K ₀
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Note 5	
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.

2. The tape with or without components shall pass around R without damage (see Figure 6).

3. If $S_1 < 1.0$ mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).

4. B, dimension is a reference dimension for tape feeder clearance only.

5. The cavity defined by A_{μ} , B_{μ} and K_{μ} shall surround the component with sufficient clearance that:

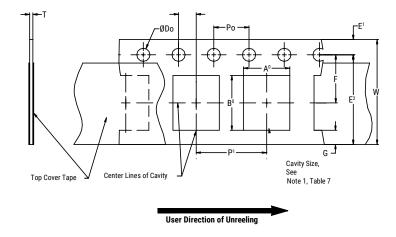
(a) the component does not protrude above the top surface of the carrier tape.

(b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

(c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).



Figure 2 – Punched (Paper) Carrier Tape Dimensions





Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 Newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of $300 \pm 10 \text{ mm/minute}$.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624*.

Figure 3 – Maximum Component Rotation

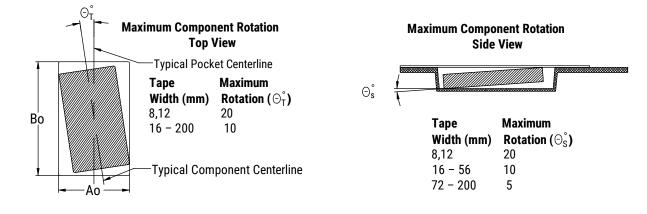


Figure 4 – Maximum Lateral Movement

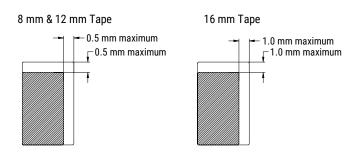


Figure 5 – Bending Radius

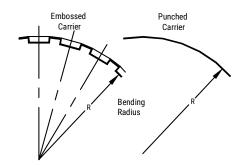
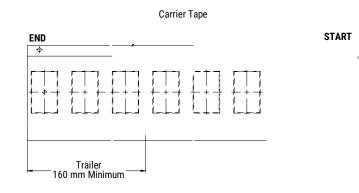




Figure 6 – Reel Dimensions



Figure 7 – Tape Leader & Trailer Dimensions





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